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An All Graphene Low Noise Amplifier

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An All-Graphene Radio Frequency Low Noise Amplifier

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ABSTRACT — In this paper, we propose and quantitatively evaluate an "All-Graphene nano-ribbon (GNR) circuit" for high frequency low noise amplifier (LNA) applications, which shows considerable advantage over state-of–the-art technologies. In particular, we discuss how to satisfy the requirements for temperature stability, gain, power dissipation, noise and speed for a high performance LNA circuit by adjusting only the width of the nano ribbons. Our calculations predict a nano-ribbon width in the range of 8-12 nm to be ideal for these types of applications – different from logic applications that are expected to require much smaller ribbon widths.

Index Terms — **graphene, RF, LNA, nano-ribbon,**

I. INTRODUCTION

Since its discovery, graphene [(1)], a single layer of graphite, has received significant scientific interest due to its remarkable electrical, chemical, mechanical and optical properties. Graphene being a gapless semiconductor is not the ideal choice for logic applications, but the high carrier mobility coupled with ballistic transport makes graphene field-effect transistors (GFETs)[(2,3,4,5)] a potential candidate for improving state-of-the-art analog technologies.

The basic building blocks of an LNA [(6)] circuit are a transistor amplifier and a load resistance, as shown in figure (1a). In this paper we discuss the implementation of the same circuit employing an "All-Graphene" approach as shown in figure (1b) and discuss how to use the width of the graphene nano ribbon as the only design parameter (figure 1c & 1d). Our analysis includes the detailed impact of the width of the graphene nano-ribbon on the voltage gain (G), speed or bandwidth (f_T), power dissipation (P), noise power (N_s) and temperature stability (S) of an RF circuit. We find that a width in the range of 8-12 nm allows for optimum performance.

II. RF LNA PARAMETERS

In this paper we will focus on low input power LNA's (maximum signal strength 100μV as found in e.g. satellite and long distance radio communications) where the most important parameters are gain (G) bandwidth (f_T), power dissipation (P), noise (N_s) and temperature used conventional equations for RF circuit parameters as defined in equation (1). g_m, g_d, R_L are the small signal transconductance, output conductance and load resistance

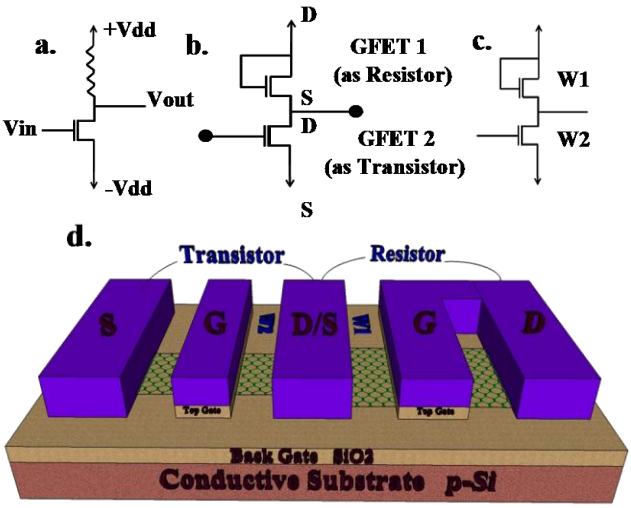

Figure 1. a) A generic LNA circuit. b) All-Graphene LNA circuit c) Width design for the All-Graphene LNA circuit. d)Layout for All-Graphene LNA circuit

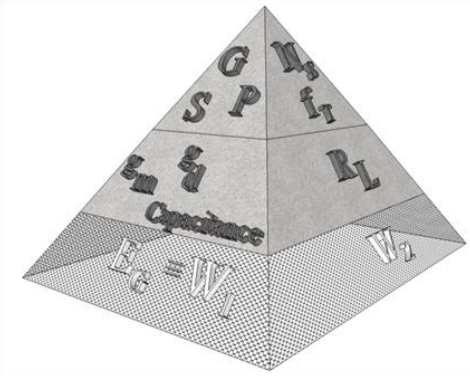

Figure 2. Top-down pyramidal view of RF-relevant variables.

$$G = \frac{g_m}{\frac{1}{R_L} + g_d} \qquad f_T = \frac{g_m}{2\pi C_{in}} \qquad P = V_{ds} I_{ds} + I_{ds}^{\ 2} R_L$$

$$N_S = 2qI_{op} f_T \qquad S = 1 - F \qquad F = \frac{1}{G}\frac{dG}{dT}\Delta T \qquad (1)$$

stability (S). We have used conventional equations for RF circuit parameters as defined in equation (1). g_m, g_d, R_L are the small signal transconductance, output conductance and load resistance of the GNRFET respectively. C_{in} is the total input capacitance including parasitic contributions due to the overlap between the gate and the source/drain contacts. In particular, C_{in} is assumed to be proportional to the channel width W which holds true if the interconnect capacitances are not the dominant part. I_{ds} and V_{ds} are the operating current and voltage for the GFET. T is the temperature and S and F denote temperature stability and temperature fluctuation respectively. ΔT is the operating temperature range for the RF device. E_G is the bandgap created in the graphene nano-ribbon due to size quantization. It has been experimentally demonstrated that the bandgap of graphene nano-ribbons is inversely proportional to the width of the ribbon [7] and the empirical relationship found is given by equation (2), where "k" is a constant. For numerical purposes, we will use k = 0.8 [7].

$$E_g = \frac{k}{W \ \ in\ nm}\ eV \qquad (2)$$

It is evident from the top down pyramidal structure that the desired conditions to enable high performance RF LNAs can be obtained by tuning the width of the nano-ribbons. The challenge is therefore to find the widths that optimize the above defined performance matrix.

III SIMULATION

Graphene is a two-dimensional hexagonal lattice exhibiting a linear energy dispersion relationship. In order to model the current transport through a graphene nano-ribbon FET we have used the Landauer Formalism [8] assuming ballistic transport conditions and ignoring contact effects that would impact the total transmission probability from source to drain.

$$I = q \int_{0}^{max(qV_{ch},qV_{ds})} D(E)v(E)\,(f_s - f_d)\,dE \qquad (3)$$

In this expression v(E) is the carrier velocity, f_S and f_d are the source and drain Fermi functions respectively, V_{ch} is the channel potential and V_{ds} is the source-to-drain potential. In our model we have also included the tunneling current across the bandgap from source to drain. The device is assumed to be operating in the quantum capacitance limit (QCL), which results in a one-to-one band control of the channel potential (V_{ch}) with the gate bias (V_{gs}) even in the on-state of the device. The transfer and output characteristics of a GNRFET are shown in figure 3.

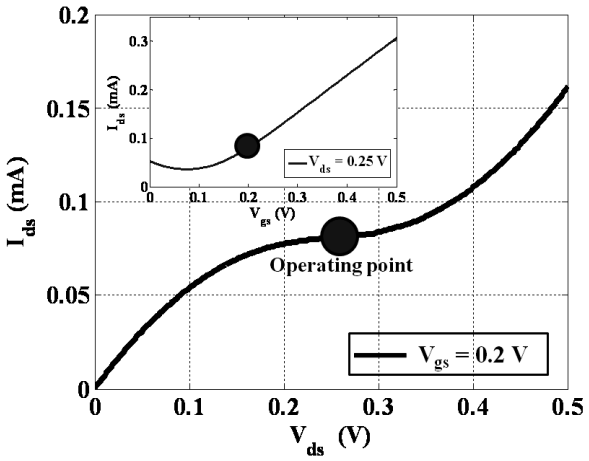

Figure 3. Output and transfer (inset) characteristics of a 10 nm wide GNRFET at room-temperature.

IV. g_m, g_d AND WIDTH (BANDGAP)

In an earlier article [9] we have identified that the transconductance and output conductance depends strongly on the dc bias point of the GNRFET of a given width and the optimum operating point for a device in the QCL is given by equation (4).

$$V_{ds} = V_{ch} + {E_g}/{2}\ , V_{gs} = V_{ch} \qquad (4)$$

In order to evaluate the impact of width (bandgap) on g_m and g_d we will assume that the GNRFET is biased at the optimum operating point, with V_{ch}=0.2V in the quantum capacitance limit for the remainder of the discussion. The transconductance g_m is directly proportional to the number of modes contributing to conduction and since the current flow is uniform across the width, the number of these conducting modes is directly proportional to the width (W) of the graphene nano-ribbon. Since W is proportional to $1/E_g$ according to equation (1), one finds that:

$$g_m \propto W \propto {1}/{E_g} \qquad (5)$$

The only way to obtain current saturation in a ballistic FET is through a sizable bandgap. The voltage span of this flat saturation region is exactly equal to the bandgap, smeared out by the broadening of the Fermi function at finite temperatures. This broadening is of the order of a few k_BT/q (k_B is Boltzmann constant). The output

conductance g_d, which is the slope of the I_d -V_{ds} curve, of such a device in the saturation region, is close to zero. But for not too aggressively scaled GNRFETs, the bandgap is of the order of k_BT at room temperature and hence g_d is finite and shows a strong temperature and bandgap dependence. The empirical relationship between temperature, bandgap and g_d is found by simulation and is given by equation (6).

$$g_d = \frac{\chi(T)}{E_g^{1.5+0.001(T-300)}} \qquad (6)$$

Equation (6) is a fit with $\chi(T)$ being a very weakly temperature dependent function which is almost constant for the temperature range considered here.

V. RF PARAMETER MATRIX AND WIDTH (BANDGAP)

The bandwidth f_T, being proportional to the ratio of g_m and C_{in}, is independent of width (bandgap), because both g_m and C_{in} are directly proportional to the width. Therefore the shot noise power N_s, which is directly proportional to the product of operating current and bandwidth, is also independent of bandgap. For a 100nm long ballistic GNRFET, f_T-values in the THz range can be expected even with moderate parasitic capacitance contribution of 100fF/μm^2.

LNAs, operating under temperature conditions ranging from -100^{0}C to +200^{0}C, require adequate temperature stability. Since the bandgap of GNRFETs is at best of the order of k_BT at room-temperature, T has a significant impact on g_d as evident from equation (6). However g_m and C_{in} are almost independent of temperature. Thus it is clear that f_T remains unaffected by temperature while the voltage gain is highly sensitive to temperature variations. We define temperature fluctuations F as in equation (7). Figure 4 shows how the stability S which is defined as 1-F, changes as a function of bandgap for different load resistances. It is evident that a larger bandgap improves stability, but the same stability value can also be achieved when using a smaller bandgap in combination with a smaller load resistance.

$$F = \frac{1}{G}\frac{dG}{dT}\Delta T = \frac{R_L}{1+R_L g_d}\frac{dg_d}{dT}\Delta T$$

$$= R_L \frac{g_d(T_{500K}) - g_d(T_{250K})}{R_L g_d(T_{300K}) + 1} \qquad (7)$$

At the optimum operating point, when the load resistance is much smaller than the output resistance, the gain G takes the form of equation (8). The inset of figure 4 shows how the gain G changes as a function of bandgap for different load resistances. It is obvious that a smaller bandgap improves gain, but the same gain can also be achieved when using a larger bandgap in combination with a larger load resistance. Table 1 summarizes the findings of this section.

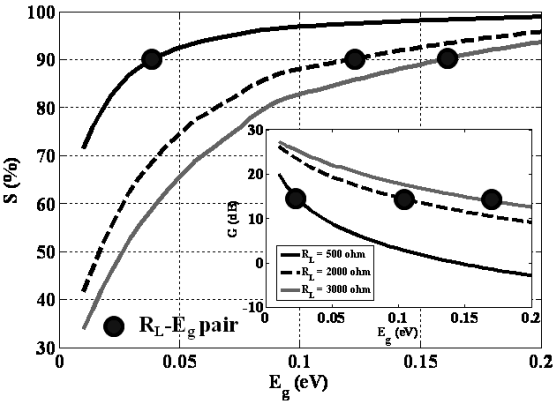

Figure 4. Stability and gain (inset) as a function of bandgap for different load resistance values R_L.

$$G = \frac{R_L g_m}{R_L g_d + 1} == \frac{R_L \gamma}{E_g} \qquad (8)$$

SUMMARY TABLE 1

	E_g	R_L
Bandwidth	***No Dependence***	***No Dependence***
Noise figure	***No Dependence***	***No Dependence***
High Gain	***Small***	***Large***
High Stability	***Large***	***Small***

VI. OPTIMIZATION

From figure 4 we conclude that a fixed value of S or G are obtained for a particular set of (R_L, E_g)-values. These (R_L, E_g) pairs can be used to construct a constant stability and constant gain contour line as shown in figure 5. Constant power dissipation (P) contours are vertical straight line with no bandgap dependence as evident from equation (9). Any (R_L, E_g) pair lying above the constant stability contour, below the constant gain contour, and on the left side of the constant power contour satisfies the demands in terms of S, G and P. In figure 5 we have used

G=15dB, S=90% and P=50μW per device and the dots represents the (R_L, E_g) pairs satisfying these particular requirements. For different values of S, G, P as demanded by the type of application under consideration one obtains

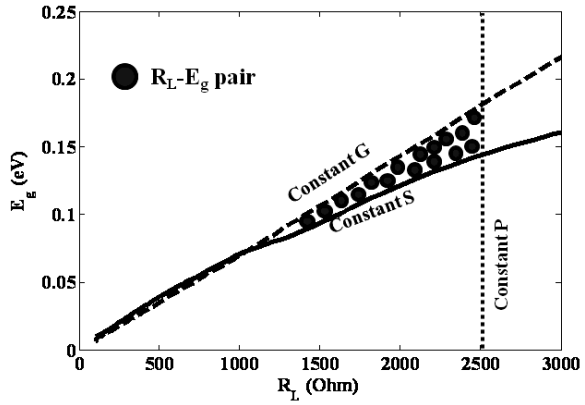

Figure 5. Constant stability, constant gain and constant power dissipation contours.

different pairs.

$$P = V_{op} I_{op} + R_L I_{op}^{\,2} \qquad (9)$$

VII. DESIGN SCHEME

Once a particular pair of (R_L, E_g)-values has been selected, the next step for a circuit engineer is to identify the associated widths of the graphene nano-ribbons (W_1, W_2) for the corresponding circuit elements, namely the load resistor and the transistor amplifier. A GNRFET with the source and gate terminal being shorted acts as a voltage dependent resistor whose resistance value is determined by the number of conducting modes and hence the width of the nano-ribbon (W_1). Figure 6a) shows the dependence of R_L on W_1 for a given bias voltage (V_{bias}). The bias voltage is the product of R_L and I_{ds} and the supply voltage (V_{DD}) is given by equation (10).

$$V_{DD} = (V_{ds} + V_{bias})/2 \qquad (10)$$

The E_g – W_2 dependence mentioned in equation (2) is depicted in figure (6b). Using figure 5 and 6, designers can easily map (R_L, E_g) pairs onto the respective (W_1, W_2) pairs.

VIII. CONCLUSION

In summary, to design an All-Graphene LNA circuit, one first has to find the optimum values of load resistance and bandgap (R_L, E_g) in order to meet the

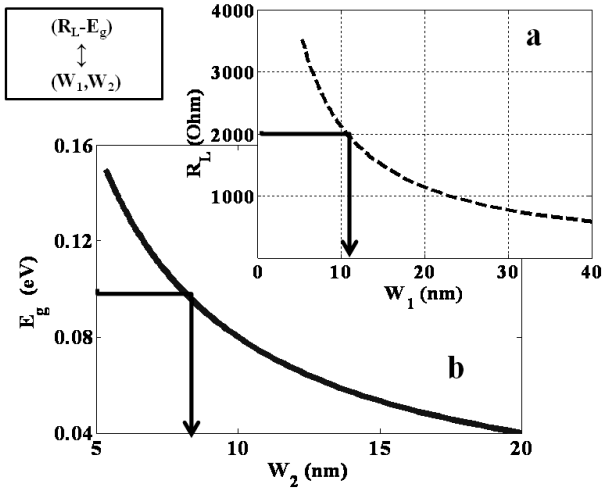

Figure 6. a) Load resistance as a function of graphene nano-ribbon width at a fixed V_{bias} b) bandgap as a function of nano-ribbon width.

requirement for a given gain, stability and power dissipation matrix (G, S, P). Then the desired (R_L, E_g) values can be adjusted by the proper choice of nano-ribbon width. For an LNA application that requires a minimum gain of 15dB and stability of 90% with power dissipation per device of less than 50μW, our findings indicate that an all graphene LNA circuit can be implemented using graphene nano-ribbons of width in the range of 8-15 nm.

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