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In highly parallel machine architectures, an important issue is how to interconnect components so as to pass data between processors. A crossbar switch connecting all the processors is very flexible, allowing any interprocessor connection. Crossbars however have cost proportional to the square of the number of processors they connect. As a consequence of the advances in microcircuitry, systems with thousands of processors are now feasible. Several less costly, and less flexible, processor interconnection networks have been proposed [3].

Of interest are the tradeoffs between the cost and the flexibility of each of the various interconnection schemes. The flexibility of an interconnection system is measured by counting the number of input to output permutations realizable by the network. In this note we count precisely the number of possible connection permutations achieved by the last stage of the Augmented Data Manipulator (ADM) introduced in [4] as a modification of the data manipulator network of [1].

In the final stage of the ADM network, a linear sequence of \( n \) components is

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connected in such a way that adjacent positions may be interchanged. The contents of the first component may also be interchanged with the last. Every set of interchanges is possible, as long as a single component does not participate in two different interchanges (else the same datum would go to two places, and the result would not be a permutation). In addition, left and right circular shifts by one position are allowed. Below we calculate precisely \( P(n) \), the number of different permutations which an ADM network of length \( n \geq 1 \) may achieve in a single step.

With each permutation performed by an ADM network of length \( n \) there is an associated bit string of length \( n \). Within the bit string, the \( i^{th} \) bit is one if and only if the \( i^{th} \) and the \( i+1^{st} \) inputs are interchanged by the associated permutation (see Figure 1).

Thus, each setting of the network, except the two circular shifts, is associated with a string of 0's and 1's with the restriction that adjacent bits may not both be 1. Let \( A(n) \) = the number of different strings of \( n \) bits in which no two adjacent bits are both 1, and the first and last bits are not both 1. Except for the trivial cases when \( n = 1 \) or 2, \( P(n) = A(n) + 2 \).
PROPOSITION 1

\[ P(1) = 1 \]
\[ P(2) = 2 \]
\[ P(n) = A(n) + 2 \text{ for } n \geq 3 \]

Proof:

\( P(1) = 1 \) is obvious. \( P(2) = 2 \) since the only two possibilities are the identity permutation and the exchange. For \( n \geq 3 \), every different setting of the ADM network of size \( n \) produces a different permutation. Consider two different settings, \( s_1 \) which interchanges \( i \) and \( i+1 \), \( s_2 \) which does not inter­change \( i \) and \( i+1 \). \( s_1 \) maps \( i \) to \( i+1 \), \( s_2 \) maps \( i \) to either \( i \) or \( i-1 \). For \( n \geq 3 \), \( i, i+1, i-1 \) are all different mod \( n \), so the two permutations are different. By the discussion above, there is a one-to-one correspondence between net­work settings, excepting the two circular shifts, and the bit strings counted in the definition of \( A(n) \).

The function \( A(n) \) is easier to analyze using a similar function \( B(n) \), which elim­inates the restriction that the first and last bits in a string must not both be 1, i.e. \( B(n) \) = the number of different strings of \( n \) bits in which no two adjacent bits are both 1.

PROPOSITION 2

\[ A(1) = 1 \]
\[ A(2) = 3 \]
\[ A(3) = 4 \]
\[ A(n) = B(n-1) + B(n-3) \text{ for } n \geq 4 \]

Proof:

The first three cases are verified by counting. For \( n \geq 4 \), consider an arbitrary string of \( n \) bits with no two adjacent 1's, including end around adja­cency. If the first bit is 0, then the remaining \( n-1 \) bits may be set in any
fashion as long as no two adjacent bits are 1. There are $B(n-1)$ such strings. If the first bit is 1, then the second and last bits must both be 0, but the remaining $n-3$ may be set in any fashion as long as no two adjacent bits are 1. There are $B(n-3)$ such strings.

**Proposition 3**

$B(n)$ is a shifted Fibonacci series.

$B(1) = 2$

$B(2) = 3$

$B(n) = B(n-1) + B(n-2)$ for $n \geq 3$

**Proof:**

For $n \geq 3$, consider an arbitrary string of $n$ bits with no two adjacent 1's. If the first bit is 0, then the remaining $n-1$ bits may be set in any fashion as long as no two adjacent bits are 1. There are $B(n-1)$ such strings. If the first bit is 1, then the second bit must be 0, but the remaining $n-2$ bits may be set in any fashion as long as no two adjacent bits are 1. There are $B(n-2)$ such strings.

Using equation (15) in [2 §1.2.B] and the above Propositions, we have that for $n \geq 6$

$$P(n) = \left[ \frac{\varphi^n}{\sqrt{5}} \right] + \left[ \frac{\varphi^{-1}}{\sqrt{5}} \right] + 2$$

where $\varphi$ is the golden ratio $\frac{1}{2}(1 + \sqrt{5})$ and $[x]$ is $x$ rounded to the nearest integer. In [5] it is shown how to count the total number of permutations produced by an ADM in terms of $P(n)$.

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References


