

11-2011

# Built-in Electric Field Minimization in (In, Ga)N Nanoheterostructure

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Liang, Zhiwen; Wildeson, Isaac; Colby, Robert; Ewoldt, David; Zhang, Tong; Sands, Timothy; Stach, Eric; Benes, Bedrich; and Garcia, R E., "Built-in Electric Field Minimization in (In, Ga)N Nanoheterostructure" (2011). *Birck and NCN Publications*. Paper 840. <http://dx.doi.org/10.1021/nl1044605>

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# Built-in Electric Field Minimization in (In, Ga)N Nanoheterostructures

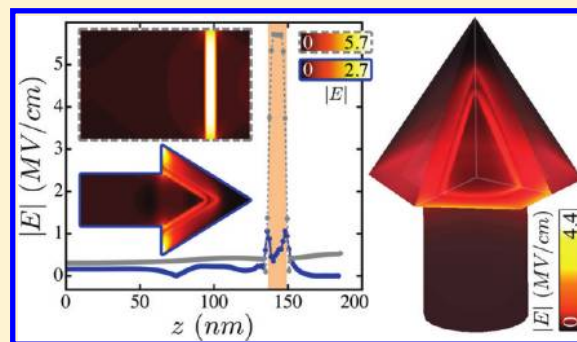
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**S** Supporting Information

**ABSTRACT:** (In, Ga)N nanostructures show great promise as the basis for next generation LED lighting technology, for they offer the possibility of directly converting electrical energy into light of any visible wavelength without the use of down-converting phosphors. In this paper, three-dimensional computation of the spatial distribution of the mechanical and electrical equilibrium in nanoheterostructures of arbitrary topologies is used to elucidate the complex interactions between geometry, epitaxial strain, remnant polarization, and piezoelectric and dielectric contributions to the self-induced internal electric fields. For a specific geometry—nanorods with pyramidal caps—we demonstrate that by tuning the quantum well to cladding layer thickness ratio,  $h_w/h_c$ , a minimal built-in electric field can be experimentally realized and canceled, in the limit of  $h_w/h_c = 1.28$ , for large  $h_c$  values.

**KEYWORDS:** (In, Ga)N nanostructures, Stark effect, (In, Ga)N pyramids, strain-induced polarization



Light emitting diodes (LEDs) based on (In, Ga)N have recently received a great deal of attention due to their potential to deliver highly efficient light emission across the entire visible spectrum without the use of phosphors.<sup>1–4</sup> The realization of such a technology has been hampered by dislocations,<sup>5–9</sup> phase segregation,<sup>10–12</sup> and piezoelectric and spontaneous polarization fields.<sup>13,14</sup> In particular, the normal component of the polarization induces discontinuities across the interfaces of the quantum well heterostructures and gives rise to built-in electric fields that separate the injected electrons and holes and reduce the recombination probability. Such an effect is known as the quantum confined Stark effect (QCSE)<sup>15–18</sup> and is believed to be one of the main limitations hindering further efficiency improvement.<sup>1–3</sup> Conventional thin film heterostructures grown on polar (0001) planes possess electric fields in their quantum wells with magnitudes on the order of MV/cm, and this effect has driven research on nonpolar and semipolar heterostructures.<sup>19–23</sup> Recent investigations of nano- and micro-sized semipolar structures demonstrate the possibility of suppressing the built-in electric fields by engineering the quantum well geometry.<sup>24–27</sup> Among these novel designs, LED heterostructures grown on pyramidal nanorods with six semipolar (1 $\bar{1}$ 01)-type facets show great promise with their low-cost fabrication based on template-assisted organometallic vapor phase epitaxy,<sup>29,30</sup> dislocation-free recombination regions,<sup>6,7</sup> and low elastic stresses allowing for a larger lattice mismatch before the incorporation of a misfit dislocation.<sup>29</sup> In this Letter, we demonstrate the possibility of dramatically reducing the QCSE within nanopyramid heterostructures. A window of realizable geometrical parameters that will lead to electric field-free

(In, Ga)N active regions is quantified. Notably, this approach is generalizable and can be applied to other III–V nanostructured materials to more deeply understand the interplay between nanoscale geometry and relevant physical properties.

In order to address the coupled effects of polarization and stress, the mechanical and electrical equilibrium are simultaneously taken into account by solving the force balance equation and Coulomb's law in its differential form:

$$\nabla \cdot \vec{D} = \rho \quad (1)$$

$$\nabla \cdot \vec{\sigma} = \vec{0} \quad (2)$$

in which  $\vec{D}$  is the electric displacement vector and  $\vec{\sigma}$  is the stress tensor.  $\vec{D}$  embodies contributions from dielectric polarization,  $P_i^{Di} = \epsilon_{ij}E_j$ , piezoelectric polarization,  $P_i^{PZ} = d_{ijk}\sigma_{jk}$ , and spontaneous polarization,  $P_i^{SP}$ , i.e.,

$$D_i = \epsilon_{ij}E_j + d_{ijk}\sigma_{jk} + P_i^{SP} \quad (3)$$

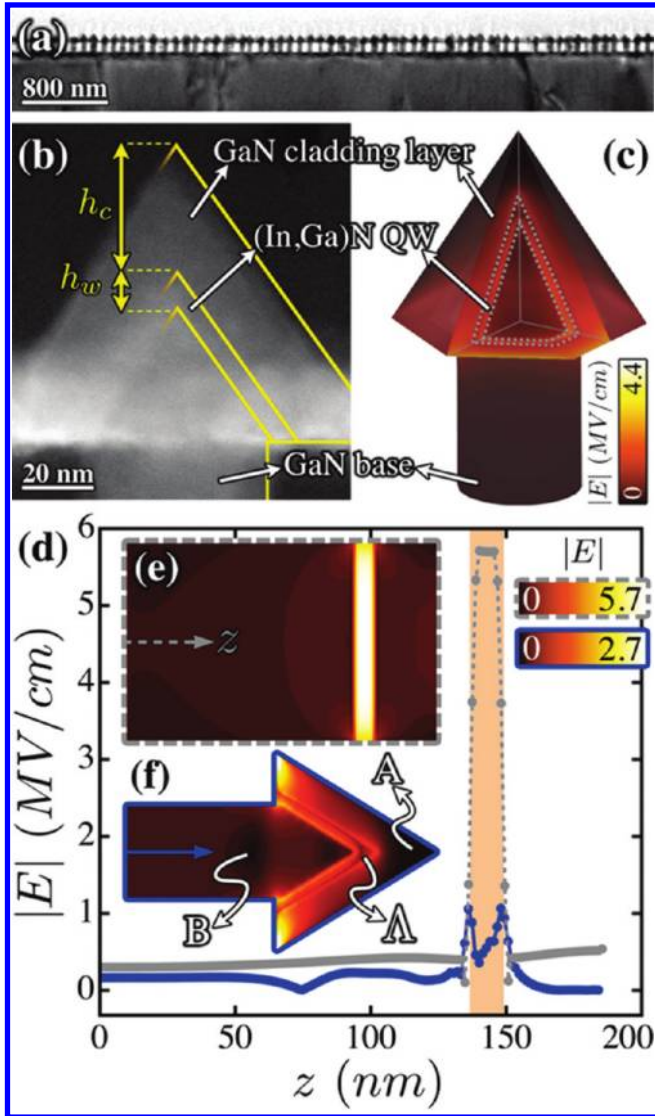
in which  $\epsilon_{ij}$  is the dielectric permittivity,  $E_j$  is the electric field, and  $d_{ijk}$  is the piezoelectric tensor.  $\sigma_{ij}$  is a result of lattice mismatch from the heterostructure as well as the converse piezoelectric effect and described by

$$\sigma_{ij} = C_{ijkl}[\epsilon_{kl}^{\text{total}} - \beta_{kl}(c - c_0) - d_{mkl}E_m] \quad (4)$$

**Received:** December 21, 2010

**Revised:** September 12, 2011

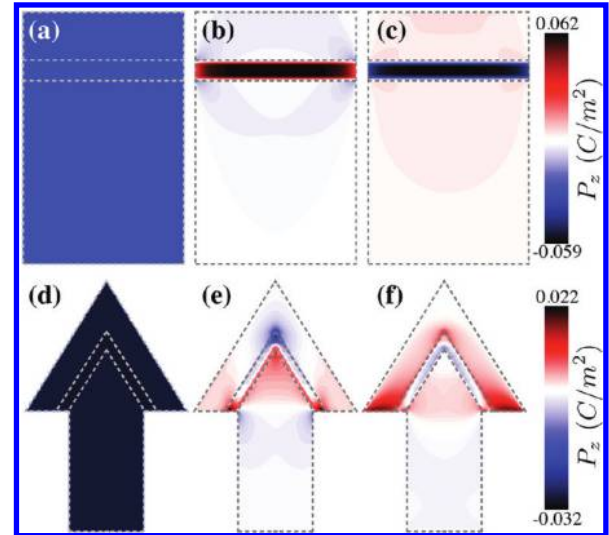
**Published:** September 26, 2011



**Figure 1.** (a) A nanorod array as observed by transmission electron microscopy. (b) A single pyramidal nanorod imaged by annular dark field scanning transmission electron microscopy illustrating a GaN base, an (In, Ga)N quantum well, and a GaN cladding layer. The boundaries of each phase have been highlighted by yellow lines in half of the structure. (c) Three-dimensional electric field magnitude,  $|E|$ , distribution within a nanopyramid heterostructure containing an  $\text{In}_{0.32}\text{Ga}_{0.68}\text{N}$  quantum well. (d)  $|E|$  along the central  $z$  axis for both nanorod and thin film heterostructures. The shaded region of the plot corresponds to the  $\text{In}_{0.32}\text{Ga}_{0.68}\text{N}$  quantum well. (e) Cross section of  $|E|$  distribution for a corresponding thin film structure. (f) Cross section of  $|E|$  distribution for the nanopyramid on a  $(10\bar{1}0)$  plane. Region  $\Lambda$  highlights the low  $|E|$  region within the quantum well, while regions A and B also show significant  $|E|$  suppression.

$C_{ijkl}$  is the stiffness tensor,  $\epsilon_{ij}^{\text{total}}$  is the total strain,  $\beta_{kl}$  is Vegard's tensor for the lattice misfit induced by the composition distribution in the heterostructure, and  $c_0$  is the equilibrium stress-free composition.

The present framework includes the (solution to the homogeneous equation) remnant polarization, piezoelectric, and dielectric contributions to the electric fields and allows the effects of the depletion zone to be included, by explicitly introducing the position-dependent, electrostatic charge contributions



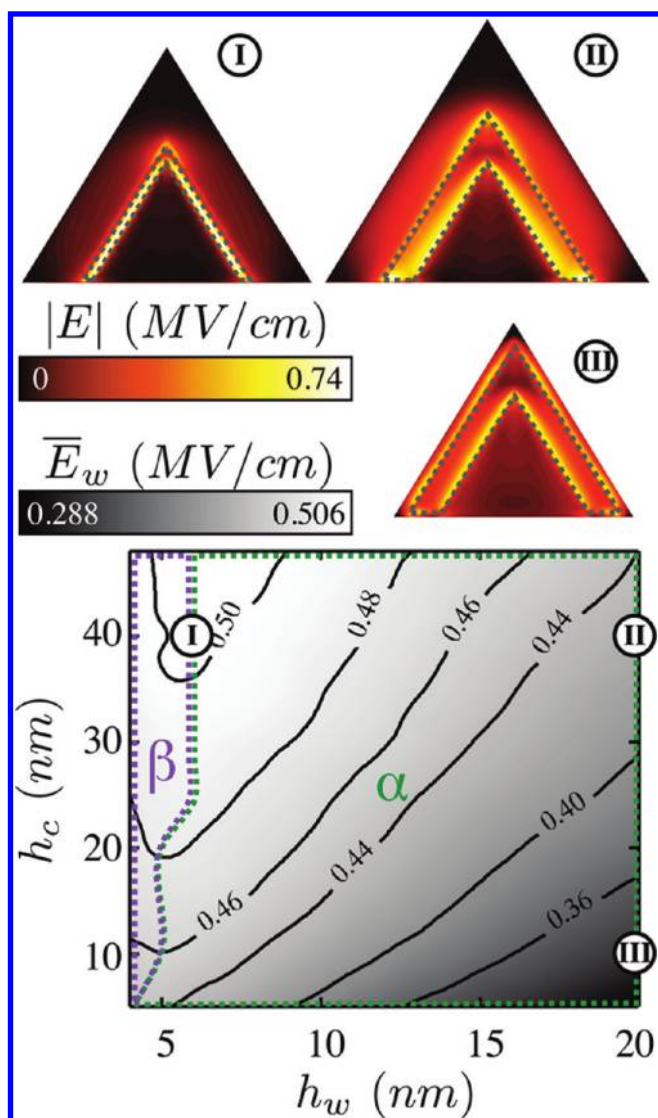
**Figure 2.** Individual polarization contributions ( $z$  component) for thin film (a–c) and pyramidal nanorod (d–f) heterostructures, each with an  $\text{In}_{0.32}\text{Ga}_{0.68}\text{N}$  quantum well. Spontaneous polarization,  $P_z^{\text{SP}}$ , corresponds to insets (a) and (d), piezoelectric contributions,  $P_z^{\text{PZ}}$ , to insets (b) and (e), and dielectric contributions,  $P_z^{\text{DI}}$ , to insets (c) and (f).

(particular solution). Both contributions to the local electrostatic potential can be solved independently as readily described in standard textbooks.<sup>28</sup> In the present formulation, the focus is on describing the dipolar effects in the aforementioned pyramidal nanoheterostructures as a necessary step to suppress the QCSE.

Equations 1 through 4 were numerically solved by using the finite volume method. The simulated system was discretized into three-dimensional regular meshes. The largest simulated mesh had 3375000 elements, required 128 gigabytes of RAM, and 47 h of wall time on an Intel server with Red Hat Enterprise Linux 5 to converge. In the Supporting Information of this Letter, the developed model was validated by directly comparing the predicted built-in field against the analytical model as described by A. E. Romanov et al.<sup>31</sup>

In this Letter, the analyzed geometry is based on structures fabricated by Wildeson et al. using template-assisted organometallic vapor phase epitaxy.<sup>29,30</sup> A porous silica mask was used to grow arrays of pyramidal nanorods, each of which consists of a GaN base, an  $\text{In}_{0.32}\text{Ga}_{0.68}\text{N}$  quantum well layer, and a GaN cladding layer (Figure 1a,b).

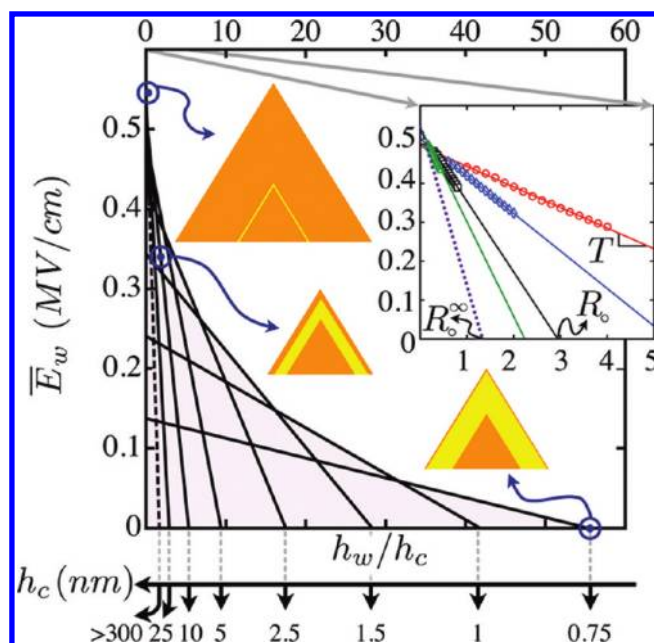
Panels c, d, and f of Figure 1 show a detailed view of the spatial distribution of the built-in electric field magnitude,  $|E|$  within the pyramidal nanoheterostructure. While the thin film structure induces a high electric field of  $\sim 5.7$  MV/cm within the quantum well (Figure 1e), the nanopyramid design results in a reduced electric field within the recombination region (Figure 1f). Results show an extended area of low electric field magnitude, on the order of 0.5 MV/cm, in the vicinity of the apex of the pyramidal quantum well, labeled herein as region  $\Lambda$ . Moreover, the lowest value of  $|E| = 0.34$  MV/cm is found within the  $\Lambda$  region, which demonstrates more than an order of magnitude reduction compared to the thin film structure. Away from the tip, since only the components of the electric field that are normal to the heterointerface induce a spatial separation of the electrons and holes, the electric field magnitude serves as an upper bound



**Figure 3.** Isocontour map of the average electric field magnitude within an  $\text{In}_{0.15}\text{Ga}_{0.85}\text{N}$  pyramidal quantum well,  $\bar{E}_w$ , as a function of the quantum well thickness,  $h_w$ , and the cladding layer thickness,  $h_c$ .  $\bar{E}_w$  is a linear function of  $h_w/h_c$  within the regime outlined as  $\alpha$ . Additional electric field reduction occurs in regime  $\beta$  because of the extremely thin quantum wells. Insets (I, II, and III) exemplify the electric field magnitude distributions for different  $h_w$  and  $h_c$  values.

estimate on the QCSE. Thus, the electron and hole wave functions within a nanopillar quantum well are expected to have an even greater overlap than what is predicted in this work. Other electric field suppressed regions include the apex of the GaN cladding layer, labeled as A in Figure 1f, and a volume along the nanorod axis a distance of 10 nm below the surface of the mask, labeled as B in Figure 1f. The highest electric field magnitude within the quantum well is found to be  $|E| = 2.7$  MV/cm, located near the bottom edge. However, the magnitude of the electric field in any of these locations is lower than its thin film counterpart due to the stress relaxation made possible by the larger surface to volume ratio of the pyramid geometry, which in turn greatly reduces the induced piezoelectric polarization.<sup>29</sup>

Figure 2 summarizes the individual contributions of polarization ( $z$  component) for both thin film and nanorod heterostructures.



**Figure 4.** Average electric field magnitude within the  $\text{In}_{0.15}\text{Ga}_{0.75}\text{N}$  pyramidal quantum well,  $\bar{E}_w$ , as a function of  $h_w/h_c$  for different  $h_c$  values. The enveloping curve of the lines in the plot shows that as the ratio of  $h_w/h_c$  increases, the maximum  $\bar{E}_w$  decreases. Representative structures are shown. The dotted line corresponds to the thick cladding layer limit. In the inset, the red open circle corresponds to  $h_c = 5$  nm, the blue open diamond corresponds to  $h_c = 10$  nm, the black open square corresponds to  $h_c = 25$  nm, while the green open triangle corresponds to  $h_c = 45$  nm.

Results demonstrate that the piezoelectric contribution that results from the coherent lattice mismatch has the most control over the built-in electric field in the system. In comparison, the contributions from the spatial discontinuity of the spontaneous polarization are much smaller. Therefore, since the exterior surfaces of the nanopillar relax the built-in stresses, the piezoelectric contribution is much lower than in a thin film, resulting in a nanopillar LED with significantly suppressed electric field when engineered appropriately. From these results, we infer that it may be possible to significantly suppress electric fields in appropriately engineered LED structures.

Experimentally, the quantum well thickness,  $h_w$ , and the cladding layer thickness,  $h_c$ , within the nanopillar heterostructures are two important parameters to be controlled during fabrication, and determine the average electric field magnitude within the quantum well,  $\bar{E}_w$  (Figure 3).  $\bar{E}_w$  is extracted within the upper 90% of the quantum well in order to avoid corner effects. Results show that thick cladding layers impose greater mechanical constraints on the quantum well and thus increase the piezoelectric polarization, which ultimately increases the electric field. Conversely, as the thickness of the cladding layer decreases,  $\bar{E}_w$  is reduced. In the limit of thick quantum wells (the  $\alpha$  regime), the larger (In, Ga)N volume allows further reduction in piezoelectric polarization, which in turn leads to  $\bar{E}_w$  suppression. In the limit of thin quantum wells (the  $\beta$  regime),  $\bar{E}_w$  is reduced because the stress state results in a favorable lowered level of piezoelectric polarization. Moreover, the electric field reduction is accompanied by an expansion of the  $\Lambda$  region from the apex down to the facets of the pyramidal quantum well (insets I, II, and III in Figure 3).

In the  $\alpha$  region, an empirical relationship between the built-in electric field and the nanopillar geometry

$$\bar{E}_w = T \left( \frac{h_w}{h_c} - R_0 \right) \text{ [MV/cm]}$$

is found, with an average deviation of 0.1% (Figure 4).

$$T = - \left( \frac{0.13}{h_c} + 0.55 \right) / \left( \frac{40.54}{h_c} + 1.28 \right) \text{ [MV/cm]}$$

represents the reduction rate of  $\bar{E}_w$  with increasing  $h_w/h_c$ , and  $R_0 = 40.54/h_c + 1.28$  corresponds to the ideal  $h_w/h_c$  ratio to specify an electric field-free structure. All possible geometries for  $\bar{E}_w$  as a function of  $h_w/h_c$  are summarized in Figure 4. Each line corresponds to a fixed  $h_c$  and specifies an optimal ratio  $h_w/h_c$ , as denoted by  $R_0$ . As a result, the ensemble of the lines summarizes a series of optimal electric field-free geometries. The enveloping curve assembled by all of the lines (shaded area in Figure 4) embodies the range of built-in electric field values accessible by the pyramidal nanorod structures and suggests that as  $h_w/h_c$  increases, the highest possible built-in electric field is reduced. For  $h_c \leq 1$  nm,  $\bar{E}_w$  is suppressed to values under 0.25 MV/cm for all  $h_w/h_c$ . As  $h_c$  increases, a larger  $T$  suggests a more significant reduction rate of  $\bar{E}_w$  as a function of  $h_w/h_c$ .

Current processing operations require thick cladding layers and thin quantum wells, which correspond to a maximum  $\bar{E}_w = 0.55$  MV/cm, a value much smaller than traditional thin film structures, which deliver  $\bar{E}_w = 2.6$  MV/cm. As a result, even in the absence of any further topology engineering, (In, Ga)N pyramidal nanorods will suppress the QCSE by an order of magnitude. In the limit of thick cladding layers,  $\bar{E}_w$  asymptotically approaches the expression (dotted line in Figure 4)

$$\bar{E}_w^\infty = -0.43 \frac{h_w}{h_c} + 0.55 \text{ [MV/cm]} \quad (5)$$

For  $h_c \geq 150$  nm,  $\bar{E}_w$  and  $\bar{E}_w^\infty$  differ from each other by less than 10%. Moreover, geometries displaying a zero built-in electric field are possible and correspond to quantum well and cladding layer thicknesses that satisfy the simple relation

$$R_0^\infty = \left. \frac{h_w}{h_c} \right|_{\text{optimal}} = 1.28 \quad (6)$$

Structures with such a thickness ratio may not be ideal from the perspective of a p–n junction device. Nevertheless, they provide insight that could be explored in other (In, Ga)N-based nano-heterostructures, potentially leading to geometries that completely eliminate polarization induced electric fields.

To summarize, we demonstrate that nanostructured (In, Ga)N-based pyramidal LEDs with semipolar facets reduce the built-in electric fields by 1 order of magnitude as compared to conventional thin film structures. Electric field magnitudes as low as 0.34 MV/cm are found within the apex of an  $\text{In}_{0.32}\text{Ga}_{0.68}\text{N}$  nanopillar quantum well, as compared to 5.7 MV/cm within a similar thin film heterostructure. Significantly reduced electric field magnitudes will alleviate the QCSE and result in more efficient LEDs. Moreover, it is demonstrated that the ratio of  $h_w/h_c$  controls the built-in polarization. In order to further suppress the built-in electric field, increasing the ratio of  $h_w/h_c$  is proposed. A set of optimal ratios are predicted to define electric field-free structures. In particular, in the limit of thick cladding layers, an optimal ratio of  $h_w/h_c = 1.28$  will completely cancel

the built-in electric field. Finally, while the predictions performed herein focus on a specific experimental geometry, they can be readily extended to describe other relevant three-dimensional nanoscale geometries in the III–V system,<sup>32–34</sup> micrometer-sized structures recently reported,<sup>35</sup> and even incorporate dislocation-free design ideas necessary in order to maximize the efficacy of the device.<sup>7</sup>

## ■ ASSOCIATED CONTENT

**S Supporting Information.** Comparison of the predicted built-in field against the analytical model as described by Romanov et al. and a table of piezoelectric constants for GaN and InN. This material is available free of charge via the Internet at <http://pubs.acs.org>.

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## ■ ACKNOWLEDGMENT

Z.L. and R.E.G. acknowledge the support of the National Science Foundation, NSF DMR 0805022, and Advanced Materials. I.H.W. would like to acknowledge funding from the NDSEG research fellowship supported by the U.S. Department of Defense.

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